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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicants:** Mikio Oda, et al.

**Examiner:** Unassigned

**Serial No.:** 10/584,736

**Art Unit:** Unassigned

**Filed:** June 26, 2006

**Docket:** 19992

**For:** OPTICAL INPUT SUBSTRATE,  
OPTICAL OUTPUT SUBSTRATE, OPTICAL  
INPUT/OUTPUT SUBSTRATE, A FABRICATION METHOD  
FOR THESE SUBSTRATES, AND AN OPTICAL ELEMENT  
INTEGRATED SEMICONDUCTOR INTEGRATED CIRCUIT

**Dated:** October 27, 2006

Mail Stop PCT  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

NOT CONSIDERED: /T.T./

NO LIST

**INFORMATION DISCLOSURE STATEMENT**

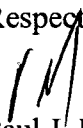
Sir:

Enclosed is a translation of the International Preliminary Report on Patentability.

Please note that the references cited in the International Preliminary Report on Patentability were filed in an Information Disclosure Statement dated June 26, 2006.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,

  
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**CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mailstop PCT, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450.

Dated: October 27, 2006

  
Paul J. Esatto, Jr.